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Docket No. 0756-1551

GAV2822
#48/IDS
Seurs
9/2/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)
Shunpei YAMAZAKI et al.) Art Unit: 2822
Serial No. 08/691,434) Examiner: M. Wilczewski
Filed: 08/02/1996)
For: METHOD OF FABRICATING)
SEMICONDUCTOR DEVICES AND)
APPARATUS FOR PROCESSING)
A SEMICONDUCTOR)

RECEIVED
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 8/23/2000.

Juli Sill

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The items marked with an asterisk (*) contained in the information disclosure statement were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

U.S. Patent 5,294,572 listed on the attached 1449 was incorrectly listed as 5,295,579 in an IDS filed May 15, 2000. Please disregard the incorrect listing and initial the correct citing as attached.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



Eric J. Robinson
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